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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Srikanth B. Samavedam et al.) Examiner: Chandra Chaudhari
)
Serial No.: 10/696,346) Group Art Unit: 2813
)
Filed: October 29, 2003) Docket No.: SC13102TP
)
Title: METHOD OF FORMING AN)
NMOS TRANSISTOR AND)
STRUCTURE THEREOF)
PROCESSING SYSTEM)

Commissioner for Patents
Alexandria, VA 22313

SUPPLEMENTAL DECLARATION TRANSMITTAL

Dear Commissioner:

Enclosed herewith is a supplemental declaration for the above-referenced patent application correcting the name of one of the inventors from Joseph C. Mogab to C. Joseph Mogab.

Respectfully submitted,

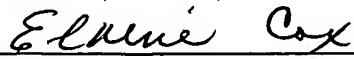
July 12, 2004
Date



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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313,

on 7/13/04
Date



Signature